

Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11) EP 0 790 762 A2

(12)

## EUROPEAN PATENT APPLICATION

(43) Date of publication:  
20.08.1997 Bulletin 1997/34

(51) Int. Cl.<sup>6</sup>: H05K 7/20

(21) Application number: 96309194.7

(22) Date of filing: 17.12.1996

(84) Designated Contracting States:  
DE FI FR GB IE SE

(72) Inventor: De Sorgo, Miksa  
Windham, New Hampshire 33087 (US)

(30) Priority: 30.01.1996 US 10830

(74) Representative:  
Purvis, William Michael Cameron et al  
D. Young & Co.,  
21 New Fetter Lane  
London EC4A 1DA (GB)

(71) Applicant: PARKER HANNIFIN CORPORATION  
Cleveland Ohio 44112 (US)

### (54) Conductive cooling of a heat-generating electronic component

(57) Conductive cooling of a heat-generating electronic component (12) disposable in spaced-apart adjacency with a surface (22) of a thermal dissipation member (14) to define a gap (24) of predetermined width (26) therebetween is provided by placing therebetween a curable composition formed of an electrically-insulating polymeric binder having thermally-conductive, electrically-insulating filler particles (36a-h) dispersed therein. At least a portion (36a-b) of the filler particles (36a-h) is of a maximum average diameter (38) about equal to the predetermined width (26) of the gap (24). A layer of the composition is disposable in

conductive heat transfer contact with the electronic component (12) and the surface (22) of the thermal dissipation member (14), and is cured to form an interlayer (30) within the gap (24). At least a portion (36a-b) of the filler particles (36a-h) of the specified maximum average diameter (38) each substantially directly contact the electronic component (12) and the surface (22) of the thermal dissipation member (14) to establish thermally-conductive pathways effective to transfer heat from the electronic component (12) to the heat dissipation member (14).

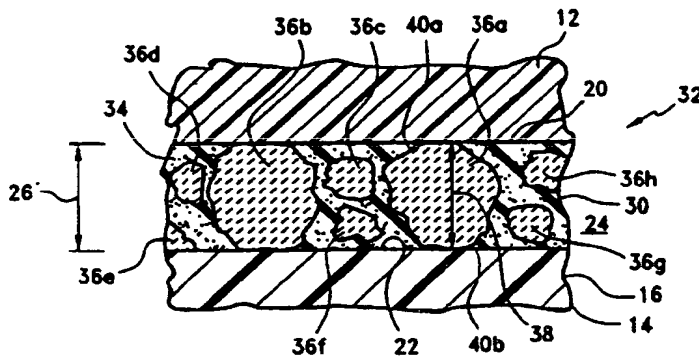


Fig. 2

EP 0 790 762 A2

## Description

The present invention relates to conductive cooling of heat-generating electronic components.

Circuit designs for modern electronic devices such as televisions, radios, computers, medical instruments, business machines, communications equipment, and the like have become increasingly complex. For example, integrated circuits have been manufactured for these and other devices which contain the equivalent of hundreds of thousands of transistors. Although the complexity of the designs has increased, the size of the devices has continued to shrink with improvements in the ability to manufacture smaller electronic components and to pack more of these components in an ever smaller area.

As electronic components have become smaller and more densely packed on integrated boards and chips, designers and manufacturers now are faced with the challenge of how to dissipate the heat which is ohmically or otherwise generated by these components. Indeed, it is well known that many electronic components, and especially semiconductor components such as transistors, are more prone to failure or malfunction at high temperatures. Thus, the ability to dissipate heat often is a limiting factor on the performance of the component.

Electronic components, such as integrated circuits, of electronic devices traditionally have been cooled via forced or convective circulation of air within a housing of the device. In this regard, cooling fins have been provided as an integral part of the component package or separately attached thereto to increase the surface area of the package exposed to convectively-developed air currents. Electric fans additionally have been employed to increase the volume of air which is circulated within the housing. For high power circuits and the smaller but more densely packed circuits typical of current electronic designs, however, simple air circulation often has been found to be insufficient adequately to cool the circuit components.

As is described in patent specifications US-A-4,965,699 and US-A-4,266,267, heat dissipation beyond that which is attainable by simple air circulation may be effected by the direct mounting of the electronic component to a "cold plate" or other heat sink. The heat sink may be a dedicated, thermally-conductive metal plate, or simply the chassis of the device. However, inasmuch as the chassis of the device generally is provided to be at ground potential, it is necessary electrically to insulate the electrical component from the chassis or other substrate to prevent shorting. In this regard, electrically-insulating, thermally-conductive interface materials have been employed between the electrical component and the heat sink. These interface materials may be compounded as containing one or more thermally-conductive particulate fillers dispersed within a polymeric binder, and may be provided in the form of cured sheets, tapes, pads, or films, or as adhesives or greases such as silicone grease. Typically binder materials include silicones, urethanes, thermoplastic rubbers, and other elastomers, with typical fillers including aluminum oxide, magnesium oxide, zinc oxide, boron nitride, and aluminum nitride.

Exemplary of the aforesaid interface materials is a boron-nitride filled silicone elastomer which is marketed under the name CHO-THERM® by the Chomerics Division of Parker-Hannifin Corp., Woburn, MA. Additionally, patent specification US-A-4,869,954 discloses a cured, form stable, sheet-like, thermally-conductive material for transferring thermal energy. The material is formed of a urethane binder, a curing agent, and one or more thermally conductive fillers. The fillers, which may include aluminum oxide, aluminum nitride, boron nitride, magnesium oxide, or zinc oxide, preferably are provided in a particle sizes ranging from about 1-50 microns (0.05-2 mils).

Patent specification US-A-4,764,845 discloses a thermally-cooled electronic assembly which includes a housing containing electronic components. A heat sink material fills the housing in direct contact with the electronic components for conducting heat therefrom. The heat sink material comprises a paste-like mixture of particulate microcrystalline material such as diamond, boron nitride, or sapphire, and a filler material such as a fluorocarbon or paraffin. The particle size distribution and packing of the heat sink material into the housing is such as to provide a mean particle to particle distance of 0.004 mils (0.1 microns).

Patent specification US-A-4,782,893 discloses a thermally-conductive, electrically-insulative pad for placement between an electronic component and its support frame. The pad is formed of a high dielectric strength material in which is dispersed diamond powder. In this regard, the diamond powder and a liquid phase of the high dielectric strength material may be mixed and then formed into a film and cured. In one embodiment, the diamond is provided as having a particle size which is the same or slightly greater than the thickness of the film. After the film is formed, a thin layer thereof is removed by chemical etching or the like to expose the tips of the diamond particles. A thin boundary layer of copper or other metal then is bonded to the top and bottom surfaces of the film such that the exposed diamond tips extend into the surfaces to provide pure diamond heat transfer paths across the film. The pad may be joined to the electronic component and the frame with solder or an adhesive.

Patent specification US-A-4,965,699 discloses a printed circuit device which includes a memory chip mounted on a printed circuit card. The card is separated from an associated cold plate by a layer of uncured silicone elastomer which is applied to the surface of the cold plate.

Patent specification US-A-4,974,119 discloses a heat sink assembly which includes an electronic component supported on a printed circuit board in a spaced-apart relationship from a heat dispersive member. A thermally-conductive, elastomeric layer is interposed between the board and the electronic component. The elastomeric member may be

formed of silicone and preferably includes a filler such as aluminum oxide or boron nitride.

Patent specification US-A-4,979,074 discloses a printed circuit board device which includes a circuit board which is separated from a thermally-conductive plate by a pre-moulded sheet of silicone rubber. The sheet may be loaded with a filler such as alumina or boron nitride.

Patent specification US-A-5,137,959 discloses a thermally-conductive, electrically insulating interface material comprising a thermoplastic or cross linked elastomer filled with hexagonal boron nitride or alumina having an average particle size diameter of from about 2-50 microns (0.08-2mils). The material may be formed as a mixture of elastomer and filler, which then may be cast or moulded into a sheet or other form.

Patent specification US-A-5,194,480 discloses another thermally-conductive, electrically-insulating filled elastomer. A preferred filler is hexagonal boron nitride having a particle size of from about 5-200 microns (2-8 mils). The filled elastomer may be formed into blocks, sheets, or films using conventional methods.

Patent specifications US-A-5,213,868 and US-A-5,298,791 disclose a thermally-conductive interface material formed of a polymeric binder and one or more thermally-conductive fillers. the fillers may be particulate solids, such as aluminum oxide, aluminum nitride, boron nitride, magnesium oxide, or zinc oxide, having a particle size of about 1-50 microns (0.05-2 mils). The material may be formed by casting or moulding, and preferably is provided as a laminated acrylic pressure sensitive adhesive (PSA) tape. At least one surface of the tape is provided as having channels or through-holes formed therein for the removal of air from between that surface and the surface of a substrate such as a heat sink or an electronic component.

Patent specification US-A-5,321,582 discloses an electronic component heat sink assembly which includes a thermally-conductive laminate formed of polyamide which underlays a layer of a boron nitride-filled silicone. The laminate is interposed between the electronic component and the housing of the assembly.

As the above-described composition have garnered acceptance for use as interface materials in conductively-cooled electronic component assemblies, it will be appreciated that further improvements in these materials and in the methods of fabricating assemblies of the same would be well-received by the electronics industry. Especially desired would be a composition which could be easily and economically applied in a fluent state directly to a circuit board, heat sink, circuit board, or the like, and then cured-in-place to provide a thermally-conductive, electrically-insulating interface material exhibiting optimum heat transfer properties.

According to one aspect of the invention there is provided a method of conductively cooling a heat-generating electronic component disposable in spaced-apart adjacency with a surface of a thermal dissipation member to define a gap of predetermined width therebetween, said method comprising the steps of:

(a) providing a curable composition comprising an electrically-insulating polymeric binder having thermally-conductive, electrically-insulating filler particles dispersed therein, at least a portion of said particles being of a maximum average diameter about equal to the predetermined width of the gap,

(b) providing a layer of said composition disposable in conductive heat transfer contact with the electronic component and the surface of the thermal dissipation member, and

(c) curing said layer of said composition to form an interlayer within the gap, at least a portion of said filler particles of said maximum average diameter each substantially directly contacting the electronic component and the surface of the thermal dissipation member to establish thermally-conductive pathways effective to transfer heat from the electronic component to the heat dissipation member.

According to another aspect of the invention there is provided a conductively-cooled electrical assembly comprising:

a thermal dissipation member having an outer surface;

a heat-generating electronic component disposed in spaced-apart adjacency with said surface of said thermal dissipation member to define a gap of predetermined width therebetween; and

a cured-in-place interlayer disposed within said gap in conductive heat transfer contact with said electronic component and the surface of said thermal dissipation member, said interlayer comprising an electrically-insulating polymeric binder with thermally-conductive, electrically-insulating filler particles dispersed therein, at least a portion of said particles being of a maximum average diameter about equal to the predetermined width of the gap and substantially directly contacting said electronic component and the surface of said thermal dissipation member to establish heat transfer pathways from said electronic component to said thermal dissipation member.

Since the fillers particles are selected such that at least a portion thereof are of a maximum average diameter which is about equal to the width of the gap, substantially direct contact is facilitated between the electronic component and the thermal dissipation member thereby to establish thermally-conductive pathways for improved transfer heat from the component to the heat dissipation member.

The curable composition may be applied in a fluent state to a surface of the electrical component or thermal dissipation member.

pation member, and then cured-in-place on that surface or interposed between the component and the dissipation member to form a thermally-conductive, electrically-insulating interlayer exhibiting improved heat transfer properties.

The invention is diagrammatically illustrated by way of example in the accompanying drawings, in which:-

Figure 1 is a fragmentary, cross-sectional view of an electrical assembly wherein an electronic component thereof can be conductively cooled in accordance with the present invention via the provision of an interlayer of a thermally-conductive, electrically-insulating curable composition between the component and an associated circuit board;

Figure 2 is a cross-sectional view of a portion of the electrical assembly of Figure 1 which is enlarged to detail the internal morphology of the interlayer composition of the present invention; and

Figure 3 is a fragmentary, cross-sectional view of an alternative embodiment of a conductively-cooled electrically assembly in accordance with the present invention wherein the thermally-conductive, electrically-insulating interlayer of Figure 1 is interposed between the electrical component and an associated heat sink.

Referring to the drawings wherein corresponding reference characters indicate corresponding elements throughout the figures, shown generally at 10 in figure 1 is an electrical assembly which includes a heat-generating, digital or analog electronic component, 12, disposed on an associated thermal dissipation member, 14. the thermal dissipation member 14 has a heat capacity relative to that of the component 12 to be effective in dissipating thermal energy transferred from the component 12. For the purposes of the present illustration, the thermal dissipation member 14 is shown as a printed circuit board or other substrate, 16, which supports the component 12 and separates it from other electronic components (not shown). The electrical component 12 may be an integrated microchip, transistor, or other semiconductor, or an ohmic or other heat-generating subassembly such as a diode, relay, resistor, transformer, amplifier, diac, or capacitor. For the electrical connection of the component 12 to the board 16, a pair of leads or pins, 18a and 18b, are provided as extending from either end of the component 12 into a soldered or other connection with the board 16. The leads 18 additionally support the component 12 above the board 16 such that an external heat transfer surface, 20, of the component 12 is spaced-apart from an opposing surface, 22, of the board 16. A gap, represented at 24 as being of a predetermined width, 26, is thereby defined between the component 12 and the board 16. For most circuit board applications, the gap width 26 typically will be about 3 mils (75 microns).

A thermally-conductive, electrically-insulating interlayer, 30, is disposed within the gap 24 in conductive heat transfer contact with the surface 20 of the component 12 and the opposing surface 22 of the board 16. The interlayer 30 at least partially fills the gap 24 to provide a conductive path therethrough for the transfer of thermal energy from the component 12 to the board 16. Such path may be employed without or in conjunction with convective air circulation to effect the cooling of the component 12 and ensuring that the operating temperature thereof is maintained below specified limits.

Referring next to Figure 2, the internal morphology of the interlayer 30 is revealed generally at 32. In general composition, the interlayer 12 is formed of continuous phase of an electrically-insulating, cured polymeric binder or matrix, 34, within which is dispersed a plurality of thermally-conductive, electrically-insulating filler particles 36a-h. The filler particles 36 may be of a substantially uniform size or, as is shown, as having a distribution which includes a range of particles sizes. In whatever form provided, however, at least a portion of the filler particles 36, represented by filler particles 36a-b, is provided to be of a maximum average diameter or diametric extent, shown at 38. which is about equal to the predetermined width 26 of the gap 24. Where the particles 36 are provided as of a uniformly graded size, substantially all of the particles will be of the specified maximum average diameter. Alternatively, where the particles 36 are provided as having a size distribution, a portion of the particles representing the maximum average diameter of the population will be of the specified maximum average diameter.

As is represented again by the filler particles 36a-b, at least a portion of those filler particles 36 having the specified dimension each are made independently within the interlayer 30 substantially directly to contact the surface 20 of the component 12 and the opposing surface 22 of the board 16. That is, with reference to particle 36a, a first end, 40a, of the particle contacts the surface 20 of the component 12, with a corresponding second end, 40b, of the particle contacting the surface 22 of the board 16. In this way, thermally-conductive pathways are established directly through the particles 36a-b to transfer heat from the component 12 to the board 16. As the filler particles 36 may be characterized as exhibiting thermal conductivities which are higher than those associated with the continuous polymeric binder phase 34, it will be appreciated that the interlayer 30 exhibits heat transfer and other thermal properties which are improved as compared to the interface materials heretofore representative of the state of the art. For example, higher bulk thermal conductivities may be realized at lower filler loadings.

The polymeric binder 34 may be a thermosetting or thermoplastic material, and may be selected from a variety of commercially-available resins and elastomers such as polyurethanes, polyimides, nylons, polyamides, polyesters, epoxies, polyolefins, polyetheretherketones, silicones, fluorosilicones, thermoplastic elastomers, acrylics, and copolymers and blends thereof. However, owing to their excellent thermal and electrical properties, silicone resins and elastomers, which may be one- or two-part systems that are room temperature vulcanizing (RTV) in the presence of a curing agent or catalyst, or that are photoinitiated or moisture cured, are to be considered preferred.

Thermally-conductive, electrically-insulating particulate solids suitable for use as the filler particles 36 include boron nitride, aluminum oxide, aluminum nitride, magnesium oxide, zinc oxide, silicon carbide, beryllium oxide, and mixtures thereof. As aforementioned, the filler particles 36 are provided as having a average particle size or maximum average size distribution selected relative to the width 26 of the gap 24 to be substantially equal thereto. For example, with the width 26 being given as about 3 mils (75 microns), the corresponding average particle or maximum average size distribution of the filler 36 will be about 3 mils (75 microns). Likewise, with the width 26 being given as about 5 mils (125 microns), the corresponding average particle or maximum average size distribution of the filler 36 will be about 5 mils (125). In contrast, most of the interface materials hereto known in the art, lacking controlled particle size distribution, have typical particle size distributions which range from about 1-1.5 mils (25-40 microns). Although there is no theoretical upper limit for either the width 25 of the gap 24, or the particle size or diametric extent 38 of the filler 36, a practical upper limit of about 8-10 mils (200-250 microns) may be realized depending upon the size availability of the selected filler, and on the limitations of the processing equipment available for compounding and delivery of the material.

The filler 36 is to be included within the polymeric binder 34 in a proportion sufficient to provide the thermal conductivity desired for the intended application. In this regard, inasmuch as there are established direct heat transfer paths through individual filler particles, the bulk thermal conductivity of the interlayer 30 is generally proportional to the filler density. For typical applications, the interlayer 30 may be provided as including about 50-55% by weight, corresponding to about 30% by volume, of the filler 36.

Additional fillers and additives may be included in the interlayer 30 to the extent that the thermal conductivity and other physical properties thereof are not overly compromised. For example, a solvent or other diluent may be employed during the compounding of the binder 34 in order to lower the viscosity thereof and improve the mixing and application/delivery of the material. Pigments, flame retardants, and antioxidants also may be added depending upon the particular application envisioned.

Turning next to Figure 3, an alternative arrangement of the conductively-cooled electrical assembly 10 is shown generally at 40. The assembly 40 again includes the heat-generating electronic component 12 which is supported on the printed circuit board 16 via the leads 18a and 18b. In the embodiment 40, however, the component 12 is interposed between the board 16 and a separate thermal dissipation member which is shown at 14' to be a separate heat sink dissipation member. The dissipation member 14' is supported over the component 12, and is shown for purposes of illustration to be configured as a thermally-conductive plate 42, which may be formed of a metal such as aluminum or other material having a relatively high heat capacity and other thermal dissipation properties.

As before, the component 12 is supported above the board 16 such that an external heat transfer surface, 20', of the component 12 is spaced-apart from an opposing surface, 22', of the plate 42. A gap, represented at 24' as being of a predetermined width, 26', is thereby defined between the component 12 and the plate 42. The thermally-conductive, electrically-insulating interlayer 30 is disposed within the gap 24' in conductive heat transfer contact with the surface 20' of the component 12 and the opposing surface 22' of the plate 42. Again, the interlayer 30 at least partially fills the gap 24' to provide a conductive path therethrough for the transfer of thermal energy from the component 12 to the plate 42. The gap 24 between the surface 20 of the component 12 and the surface 22 of the board 16 may be left unfilled further to assist the conductive cooling of the component 12. Alternatively, the gap 24 may be filled, as was described in connection with Figure 1, with a second interlayer 30 (not shown) to provide a second conductive heat transfer path from the component 12.

The interlayer 30 may be incorporated with the assemblies 10 or 40 as being formed from a layer of a curable composition which is applied in a fluent state to the surface 22 of the board 16 (Figure 1) prior to the mounting of the component 12 thereon, or to the surface 20' of the component 12 (Figure 3) prior to the disposition of the component 12 in a spaced-apart adjacency from the heat sink plate 42. In both of the described variations, the applied layer of the curable composition may be cured either prior to assembly or, after assembly, in situ within the gap 24 (Figure 1) or 24' (Figure 3). By "cured," it is meant that the binder of the composition is polymerized, cross-linked, further cross-linked or polymerized, vulcanized, cooled, hardened, or otherwise chemically or physically changed from a liquid or other fluent into a solid resin, elastomeric, or other polymeric phase.

Alternatively, the interlayer 30 may be provided as being formed from a layer of a curable composition which is injected or extruded in a fluent state into the gap 24 of the assembly 10 (Figure 1), or the gap 24' of the assembly 10 (Figure 3), wherein which it is cured in situ. In all of the above-described variations, it will be appreciated that the interlayer 30 is cured-in-place on either a surface of the component 12 or the thermal dissipation member 14 or 14', or in situ between the component 12 and the thermal dissipation member 14 or 14'. In this regard, the necessity to pre-form the interface material as a sheet or pad, and then to apply the material to the component or dissipation member as separate steps is obviated. Moreover, waste generation is reduced with the present flashless process as compared to die cut or moulding processes. Such a cure-in-place process additionally is less labour intensive as being amenable to a more automated operation.

In a preferred embodiment, the thermally-conductive interlayer is provided as formed-in-place from a curable composition which comprises a binder of an RTV silicone elastomer loaded with about 50-55wt% of graphitic boron nitride (BN) filler particles. The BN filler is selected as having a thermal conductivity of about 25-50 W/m<sup>2</sup>°K, and is milled or

otherwise processed to have a maximum particle size distribution of at least about 3 mils (75 microns) and, desirably, at least about 5 mils (125 microns). Suitable RTV silicones are marketed commercially by Dow Corning Corp (Midland, MI), General Electric Co (Silicone Products Div., Pittsfield, MA), and Shinetsu Corp. (Torrance, CA). Preferably, the silicone binder is provided as a two-part formulation of two reactive components and a platinum catalyst.

Suitable BN fillers are marketed commercially by Advanced Ceramic Corp. (Cleveland, OH). The filler is available as a mixture which contains a particle size distribution of 14-15 mils (355-380 microns) maximum, 8-9 mils average (200-230 microns), and 3-4 mils (75-100 microns) fines. Each of the reactive components is pre-mixed with the BN fillers particles using a planetary mixer or the like. The components then are separately passed through a 3-roll mill or the like to reduce the particle size of the filler to about a 4-5 mil (100-125 microns) maximum. In this regard, the rolls of the mill are set to have a gap of about 5 mils (125 microns) therebetween to achieve the characteristic controlled particle size. This is to be contrasted with a conventional milling process which would reduce the size of the filler down to elementary particles of about a 1-1.5 mil (25-40 microns) maximum.

After compounding, the components may be stored for later reactive processing using a hand-held caulking gun, which may be of a cartridge, fed, static-mixer variety, or, alternatively, using automated injection equipment such as a robotic applicator. Immediately prior to application, the components are admixed and then delivered as a reactive mixture to a surface of the electrical assembly wherein the mixture is cured-in-place to form the thermally-conductive interlayer. To reduce the cycle time, the curing may be effected at an elevated temperature to accelerate the vulcanization reaction. The components alternatively may be pre-mixed and then frozen for storage. To facilitate mixing and application, it is preferred that the admixture is formulated to have a viscosity of about 300,000 cp. It has been empirically observed that the relatively larger filler particle size of the composition achieves fluent viscosities at higher filler loading proportions, e.g., 50-55wt%, as compared to the compositions heretofore known in the art which are characteristically loaded at 35-40wt%. Moreover, as the graphitic BN filler of the preferred embodiment may be considered a lubricant relative to the abrasive oxide fillers common in conventional formulations, improved tool life is realized for the mixing, metering, and delivery equipment which may be employed.

As an alternative to the above-described 2-part RTV system, a one-part system may be provided as comprising, for example, a hydrolyzable polyfunctional silane or siloxane which is activated by atmospheric moisture. In whatever form the silicone-based system is provided, due to the affinity of the silicone polymer for most surfaces, excellent adhesion of the cured material to substrates may be realized without the use of primers or the like. If desired, however, a primer such as a functionalized silane, silicate ester, cyanurate, or the like may be used.

The Example to follow, wherein all percentages and proportions are by weight unless otherwise expressly indicated, is illustrative of the practising of the invention herein involved, but should not be construed in any limiting sense.

#### EXAMPLE

A two-part, RTV silicone-based curable composition for forming the thermally-conductive, electrically-insulative interlayer was compounded as is set forth in Table 1:

TABLE 1  
Preparation of 2-Part RTV Formulation

Part A:	
200 parts silicone resin (GE 615A, GE Silicone Products Div)	
225 parts boron nitride particles (HCM-grade, Advanced Ceramics)	
Part B:	
200 parts silicone resin (GE 615B, GE Silicone Products Div)	
225 parts boron nitride particles (HCM-grade, Advanced Ceramics)	
10 parts blue pigment (D 4900 grade, Harwick Chemical)	
particle size distribution of 14-15 mils (355-380 microns) maximum, 8-9 mils average (200-230 microns), and 3-4 mils (75-100 microns) fines	

Each of Parts A and B was prepared in a Ross double planetary mixer (Charles Ross & Sons) by premixing the BN filler into the silicone resin until homogeneous. The pre-mixed components were then separately passed through a

Ross three roll mill (Charles Ross & Sons). The rolls of the mill were set as having a gap spacing of about 5 mils in order to effect the reduction of the filler to about a 4-5 mil (100-125 mil) maximum.

Formulated as described, the Part A and B components then were loaded into a syringe-or caulking-type gun (Charles Ross & Sons) having a static mixing nozzle and a dual piston displacement set for a mix ratio of 10:1 by volume of A:B, i.e., 38 cc Part A to 3.8 cc Part B. A quantity of the mixed material was delivered from the gun and cured at room temperature for 48 hours. The following physical properties are representative of the cured composition, and confirm the suitability thereof for the intended application:

TABLE 2

Representative Physical Properties	
Volume Resistivity	$10^{13} \Omega\text{-cm}$
Dielectric Strength	500 VAC/mil
Hardness	70 Shore A
Thermal Conductivity	1.2 W/m-°K
Specific Gravity	1.50

### Claims

1. A method of conductively cooling a heat-generating electronic component (12) disposable in spaced-apart adjacency with a surface (22,22') of a thermal dissipation member (14,14') to define a gap (24,24') of predetermined width (26,26') therebetween, said method comprising the steps of:
  - (a) providing a curable composition comprising an electrically-insulating polymeric binder having thermally-conductive, electrically-insulating filler particles (36a-h) dispersed therein, at least a portion (36a-b) of said particles (36a-h) being of a maximum average diameter (38) about equal to the predetermined width of the gap (24,24'),
  - (b) providing a layer of said composition disposable in conductive heat transfer contact with the electronic component (12) and the surface (22,22') of the thermal dissipation member (14,14'), and
  - (c) curing said layer of said composition to form an interlayer (30) within the gap (24,24'), at least a portion of said filler particles (36a-b) of said maximum average diameter (38) each substantially directly contacting the electronic component (12) and the surface (22,22') of the thermal dissipation member (14,14') to establish thermally-conductive pathways effective to transfer heat from the electronic component (12) to the heat dissipation member (14,14').
2. A method according to claim 1, wherein the thermal dissipation member is a heat sink (14') or a circuit board (14).
3. A method according to claim 1, wherein said layer of said composition is provided in step (b) as applied on the surface of the thermal dissipation member (14,14'), and wherein said method further comprises an additional step after step (b) of disposing said electronic component (12) on said layer.
4. A method according to claim 1, wherein said layer of said composition is provided in step (b) as applied to an outer surface of said electronic component (12), and wherein said method further comprises an additional step after step (b) of disposing said thermal dissipation member (14') on said layer.
5. A method according to claim 1, further comprising an additional step prior to step (b) of disposing the electronic component (12) in said spaced-apart adjacency with the surface of the thermal dissipation member (14,14'), and wherein said layer is provided in step (b) as interposed between said electronic component (12) and the surface of the thermal dissipation member (14,14').
6. A method according to claim 1, wherein said curable composition is provided in step (a) as comprising a first and a second reactive component each containing a portion of said filler particles (36a-h), said method further comprising an additional step prior to step (b) of admixing said first and said second reactive component.

7. A method according to claim 6, wherein said first reactive component comprises a silanol-terminated polymer and said second reactive component comprises a cross-linking agent, said reactive components combining when admixed to form a room-temperature vulcanizing (RTV) silicone elastomer forming said polymeric binder of said composition.
8. A method according to claim 6, further comprising an additional step prior to step (b) of disposing the electronic component (12) in said spaced-apart adjacency with the surface of the thermal dissipation member (14,14'), and wherein said layer is provided in step (b) by injecting the admixed first and second reactive components between the electronic component (12) and the surface of the thermal dissipation member (14,14').
9. A method according to claim 1, wherein said filler particles (36a-h) of said curable composition are selected from the group consisting of particles of boron nitride, aluminum oxide, aluminum nitride, magnesium oxide, zinc oxide, silicon carbide, beryllium oxide, and mixtures thereof.
10. A method according to claim 1, wherein said filler particles (36a-h) comprise graphitic boron nitride.
11. A method according to claim 1, wherein said polymeric binder of said curable composition is selected from the group consisting of polyurethanes, polyimides, nylons, polyamides, polyesters, polyolefins, epoxies, polyetheretherketones, silicone elastomers, fluorosilicone elastomers, thermoplastic elastomers, acrylic resins, and copolymers and blends thereof.
12. A method according to claim 1, wherein said polymeric binder of said curable composition comprises a room temperature vulcanizing (RTV) silicone elastomer.
13. A method according to claim 1, wherein said curable composition is provided as comprising about 50 to 55% by weight of said filler particles (36a-h).
14. A method according to claim 1, wherein the width of the gap (24,24') between the electronic component and the surface of the thermal dissipation member (14,14') is at least about 3 mils (75 microns).
15. A method according to claim 1, wherein the average diameter of the portion (36a-b) of said particles (36a-h) of said curable composition provided as having a maximum average diameter (38) substantially equal to the predetermined width of the gap (24,24') is at least about 5 mils (125 microns).
16. A conductively-cooled electrical assembly (10) comprising:
  - a thermal dissipation member (14,14') having an outer surface (22,22');
  - a heat-generating electronic component (12) disposed in spaced-apart adjacency with said surface (22,22') of said thermal dissipation member (14,14') to define a gap (24,24') of predetermined width (26,26') therebetween; and
  - a cured-in-place interlayer (30) disposed within said gap (24,24') in conductive heat transfer contact with said electronic component (12) and the surface (22,22') of said thermal dissipation member (14,14'), said interlayer (30) comprising an electrically-insulating polymeric binder with thermally-conductive, electrically-insulating filler particles (30a-h) dispersed therein, at least a portion (30a-b) of said particles being of a maximum average diameter (38) about equal to the predetermined width (26,26') of the gap (24,24') and substantially directly contacting said electronic component (12) and the surface (22,22') of said thermal dissipation member (14,14') to establish heat transfer pathways from said electronic component (12) to said thermal dissipation member (14,14').
17. An electrical assembly according to claim 16, wherein said thermal dissipation member is a heat sink (14') or a circuit board (14).
18. An electrical assembly according to claim 16, wherein said filler particles (36a-h) of said interlayer (30) are selected from the group consisting of particles of boron nitride, aluminum oxide, aluminum nitride, magnesium oxide, zinc oxide, silicon carbide, beryllium oxide, and mixtures thereof.
19. An electrical assembly according to claim 16, wherein said filler particles (36a-h) of said interlayer (30) comprise graphitic boron nitride.



20. An electrical assembly according to claim 16, wherein said polymeric binder of said interlayer (30) is selected from the group consisting of polyurethanes, polyimides, nylons, polyamides, polyesters, polyolefins, epoxies, polyetheretherketones, silicon elastomers, fluorosilicon elastomers, thermoplastic elastomers, acrylic resins, and copolymers and blends thereof.
21. An electrical assembly according to claim 16, wherein said polymeric binder of said interlayer (30) comprises a room temperature vulcanizing (RTV) silicon elastomer.
22. An electrical assembly according to claim 16, wherein said interlayer (30) comprises about 50 to 55% by weight of said filler particles (36a-h).
23. An electrical assembly according to claim 16, wherein the width (26,26') of said gap (24,24') between said electronic component (12) and the surface (22,22') of said thermal dissipation member (14,14') is at least about 3 mils (75 microns).
24. An electrical assembly according to claim 16, wherein the average diameter of the portion (36a-b) of said particles (36a-h) of said interlayer (30) having a maximum average diameter substantially equal to the predetermined width (26,26') of said gap (24,24') is at least about 5 mils (125 microns).

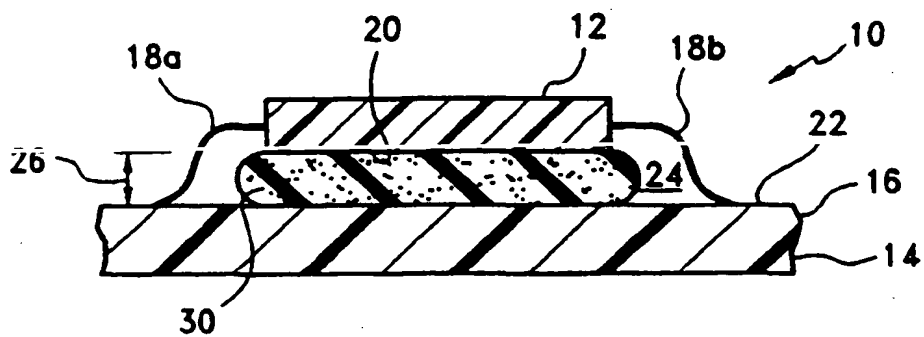


Fig. 1

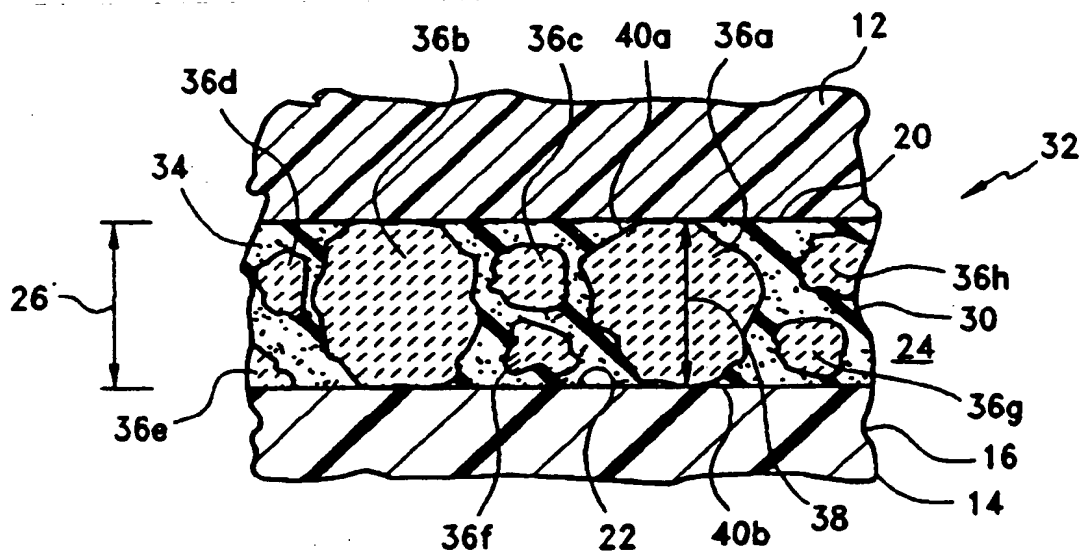


Fig. 2

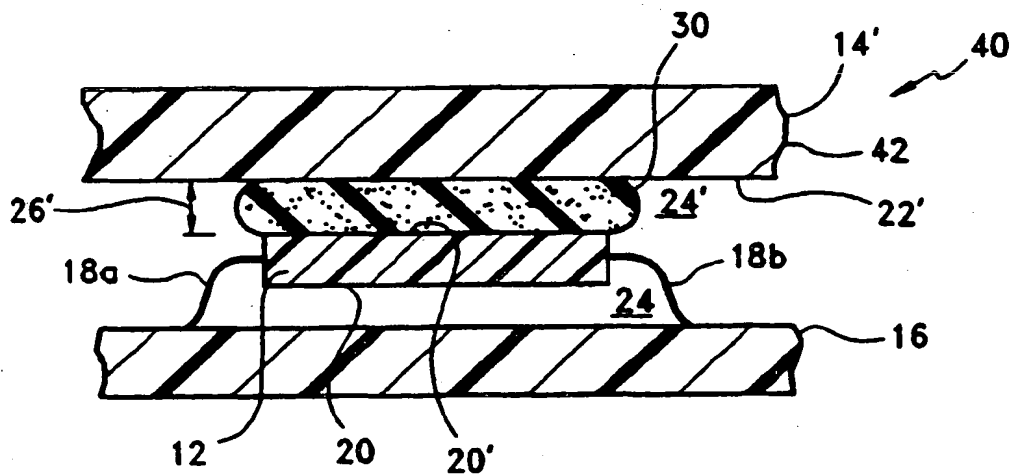
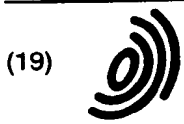


Fig. 3



(19)

Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 0 790 762 A3

(12)

## EUROPEAN PATENT APPLICATION

(99) Date of publication A3:  
13.01.1999 Bulletin 1999/02

(51) Int. Cl. 5: H05K 7/20, B32B 5/16

(43) Date of publication A2:  
20.08.1997 Bulletin 1997/34

(21) Application number: 96309194.7

(22) Date of filing: 17.12.1996

(84) Designated Contracting States:  
DE FI FR GB IE SE

(30) Priority: 30.01.1996 US 10830

(71) Applicant:  
PARKER HANNIFIN CORPORATION  
Cleveland Ohio 44112 (US)

(72) Inventor: De Sorgo, Miksa  
Windham, New Hampshire 33087 (US)

(74) Representative:  
Purvis, William Michael Cameron et al  
D. Young & Co.,  
21 New Fetter Lane  
London EC4A 1DA (GB)

## (54) Conductive cooling of a heat-generating electronic component

(57) Conductive cooling of a heat-generating electronic component (12) disposable in spaced-apart adjacency with a surface (22) of a thermal dissipation member (14) to define a gap (24) of predetermined width (26) therebetween is provided by placing therebetween a curable composition formed of an electrically-insulating polymeric binder having thermally-conductive, electrically-insulating filler particles (36a-h) dispersed therein. At least a portion (36a-b) of the filler particles (36a-h) is of a maximum average diameter (38) about equal to the predetermined width (26) of the gap (24). A layer of the composition is disposable in

conductive heat transfer contact with the electronic component (12) and the surface (22) of the thermal dissipation member (14), and is cured to form an interlayer (30) within the gap (24). At least a portion (36a-b) of the filler particles (36a-h) of the specified maximum average diameter (38) each substantially directly contact the electronic component (12) and the surface (22) of the thermal dissipation member (14) to establish thermally-conductive pathways effective to transfer heat from the electronic component (12) to the heat dissipation member (14).

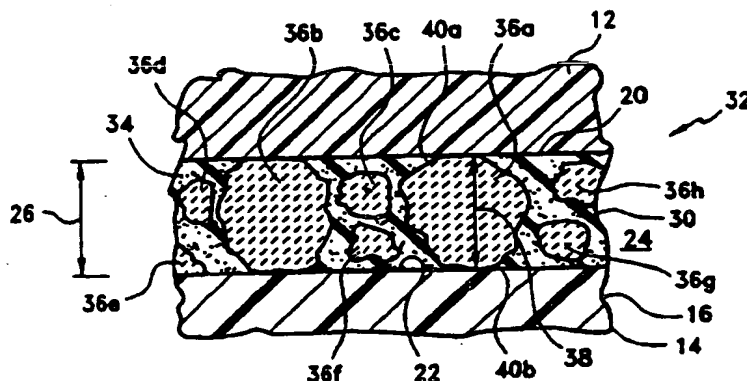


Fig. 2

EP 0 790 762 A3

European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number

DOCUMENTS CONSIDERED TO BE RELEVANT			EP 96309194.7
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 6)
D, Y	US 4869954 A (SQUITTIERI) 26 September 1989 (26.09.89), abstract, column 1, column 2, lines 39-45, column 3, lines 31-35, column 4, lines 3-16, claims	1, 2, 6, 7, 9, 11-24	H 05 K 7/20 B 32 B 5/16
A	--	3-5, 8, 10	
D, Y	US 5194480 A (BLOCK) 16 March 1993 (16.03.93), abstract, column 2, line 6 - column 4, line 2, examples, claims.	1, 2, 6, 7, 9, 11, 12, 14-24	
A	--	5	
D, Y	US 5137959 A (BLOCK) 11 August 1992 (11.08.92), totality.	1, 6, 7, 9, 11, 12, 14-24	TECHNICAL FIELDS SEARCHED (Int. Cl. 6)
A	--	5	
D, Y	US 4782893 A (THOMAS) 08 November 1988 (08.11.88), figs., abstract, column 2, line 58 - column 4, line 31, claims.	1, 2, 6, 9, 11, 13-20, 22-24	H 05 K 7/00 B 32 B 5/00
A	--	3-5	
D, Y	US 5213868 A (LIBERTY) 25 May 1993 (25.05.93), figs., abstract, column 6, line 30 - column 8, line 21, claims.	1, 2, 6, 9, 11, 14-20, 22-24	
A	----	5	
The present search report has been drawn up for all claims			
Place of search VIENNA		Date of completion of the search 31-07-1998	Examiner KRAL
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1503 01.82 (P0401)